

EAST - [default.wsp:1]

File View Edit Tools Window Help

Drafts
Pending
Active
L1: (201) memory same control\$3 same activat\$3 same
L2: (139) 1 same (time or tuning)
Failed
Saved
Favorites
Tagged (3)
UDC

DBs: USPAT; US-PGPUB; EPO; JPO
Default operator: OR
Highlight all hit terms initially

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	201	memory same control\$3 same activat\$3 same bit same prior same select\$3	USPAT; US-PGPUB; EPO; JPO
2	BRS	L2	139	1 same (time or tuning)	USPAT; US-PGPUB; EPO; JPO

Start Inb Exp C.L. eD Do C.L. EA EA 5:25

EAST - [default.wsp:1]

File View Edit Tools Window Help

Drafts
Pending
Active
L1: (8) (column adj switch)
Failed
Saved
Favorites
Tagged (0)
UDC
Queue

DB: USPAT,US PGPUB,EPO,JPO
Default operator: OR
Highlight all hit terms initially

(column adj switch\$3) same ((before or prior) near3 (wordline or (word adj line) or word-line))

ABSTRACT EDIT IMAGE TEXT HTML

	U	Document ID	Issue Date	Pages	Title	Abstract	Current OR	Current XR
1	<input type="checkbox"/>	US 6288928 B1	20010911	33	Semiconductor integrated circuit and method of controlling column switch of semiconductor integrated circuit in write operation		365/63	365/190; 365/203; 365/205
2	<input type="checkbox"/>	US 5629899 A	19970513	17	Semiconductor memory		365/230.03	365/149; 365/189.04
3	<input type="checkbox"/>	US 5404337 A	19950404	20	Semiconductor memory		365/230.03	365/149; 365/189.04
4	<input type="checkbox"/>	US 5313434 A	19940517	21	Semiconductor memory device		365/233.5	365/190; 365/203
5	<input type="checkbox"/>	US 5249159 A	19930928	18	Semiconductor memory		365/230.05	365/189.04; 365/218
6	<input type="checkbox"/>	US 5042013 A	19910820	18	Semiconductor memory		365/230.05	365/189.04; 365/233
7	<input type="checkbox"/>	US 4916669 A	19900410	18	Semiconductor memory		365/230.05	365/189.04
8	<input type="checkbox"/>	JP 03108187 A	19910508	6	SEMICONDUCTOR STORAGE DEVICE			365/203

Start

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